#8|D

7-3-03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	Examiner: Lao, Sue X.
)	
Anderson, et al.)	Art Unit: 2126
)	
Serial No. 09/007,019)	
)	
Filed: January 14, 1998)	
)	
For: EXECUTION CONTROL FOR)	
PROCESSOR TASKS)	
)	

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner for Patents, PO Box 1450, Alexandria, Virginia 22313-1450

on _____ June 23, 2003 _____
Date of Deposit

_____ Beverly Kehoe Shea _____
Name of Person Mailing Correspondence

_____ 6/23/03 _____
Signature   Date

MS Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RECEIVED

JUL 0 2 2003

Technology Center 2100

AMENDMENT AND RESPONSE TO OFFICE ACTION

In the Claims

1. (Currently Amended) A method in a computer system of executing a first sequence of modules in a first task, said first sequence of modules linked to one another and having at least one sequence of execution, comprising:

    a.   storing in each of said first sequence of modules a skip value indicating a next module in said first sequence of modules to execute;

    b.   executing a first module of said first sequence of said modules; and

    c.   executing said next module of said first sequence of modules indicated by the skip value stored in a currently executed module, skipping any module between the currently executed module and said next module, wherein each module of said first sequence of modules comprises at least one digital signal processing data structure.